

QGen

A qualifiable and customizable code generator from Simulink® and Stateflow® models to MISRA C and SPARK

QGen is a qualifiable and tunable code generation and model verification tool for a safe subset of Simulink® and Stateflow® models. It reduces the development and verification costs for safety-critical applications through qualifiable code generation, model verification, and tight integration with AdaCore's qualifiable simulation and structural coverage analysis tools.

Major Benefits Include:

- Treatment of safe subset of Simulink® blocks
- Code generation for MISRA C and SPARK
- Support for DO-178B/C, EN 50128, ISO 26262
- Formal model verifier
- Open, extendible framework for model integration
- Integration with AdaCore's compilation, simulation and testing tools
- Support for Processor-in-the-Loop testing via GNATemulator

To learn more about this technology and its benefits, please visit www.adacore.com/qgen